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drop doorbell buffer register queue

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1.22001/10/02 23:05:14 * linux/include/linux/cyclades.h * * This ...
struct CUSTOM_REG { uclong fpga_id; /* FPGA Identification Register */ uclong Tx
buffer */ #define C_CM_Q_ENABLE 0x58 /* enables queue access from the ...
[cvs.sourceforge.jp/cgi-bin/viewcvs.cgi/uclinux-h8/uClinux-2.0.x/include/linux/cyclades.h?](http://cvs.sourceforge.jp/cgi-bin/viewcvs.cgi/uclinux-h8/uClinux-2.0.x/include/linux/cyclades.h?rev=1.1.1.1)
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Re: [ofa-general] post_recv question | KernelTrap

36 posts - Last post: Feb 21, 2008

When you post to a receive queue, the buffer does not belong the ... post_recv code rings
a doorbell (write to adapter mem or register) t3: ...

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Re: [ofa-general] post_recv question | KernelTrap

... hw work queue t2: post_recv code rings a doorbell (write to adapter mem or register) t3:
post_recv returns t4: <app assumes the buffer is ready>> ...

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Source Code, Sample Code or Example Code For writel

sa_writel(dev, Mailbox4, 0); /* * Clear the synch command doorbell to start on a
vb.queue); q->curr = buf; DEB_INT(("next buffer: buf:%p, prev:%p, ...
www.codase.com/search/cal?name=writel - 67k - [Cached](#) - [Similar pages](#)

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HCA and register a callback handler with CQ. Completion queue can also be accessed
through buffer and ringing the doorbell to notify the ...

<ftp://ftp.cse.ohio-state.edu/pub/tech-report/2005/TR63.pdf> - [Similar pages](#)

BSDCon '03 — Technical Paper

In addition to context switch overhead, the memory buffer used to stage data to and from ...
unit doorbell or queue pointer register is written is critical. ...

www.usenix.org/events/bsdcon03/tech/full_papers/thorpe/thorpe_html/index.html - 146k -
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GIT e4f775874f071120204c58a21eafe132ca5f2396 master.kernel.org ...
+ */ + + /* disable the RAM Buffer receive queue */ + sky2_write8(hw, RB_ADDR(rxqaddr
[port], RB_CTRL), RB_DIS_OP_MD); + + for (i = 0; i < 0xffff; ...
[www.kernel.org/pub/linux/kernel/people/akpm/patches/2.6/2.6.14-rc1/2.6.14-rc1-](http://www.kernel.org/pub/linux/kernel/people/akpm/patches/2.6/2.6.14-rc1/2.6.14-rc1-mm1/broken.../git-netdev-all.patch)
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Out-of-Sequence Tx Data Buffer Pointer Register (OSTBDP)..... 14-40

Doorbell Queue Enqueue Pointer Address Register (DQEPAR)

www.freescale.com/files/32bit/doc/ref_manual/MPC8540RM.pdf - [Similar pages](#)

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send/recv ticket ring. send/recv. queue. descriptor. buffer public ViJbufTicket register(Vi
vi); /* reg + pin jbuf */ ...

www.cs.cornell.edu/Info/People/chichao/talk-final.ppt - [Similar pages](#)

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Although described as a "register," the data to be stored in doorbell register 620 As
illustrated, a set of output queues 860 may temporarily buffer the ...

www.wipo.int/pctdb/en/wo.jsp?WO=2004093378&IA=US2004010912&DISPLAY=DESC -
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